FIG. 1

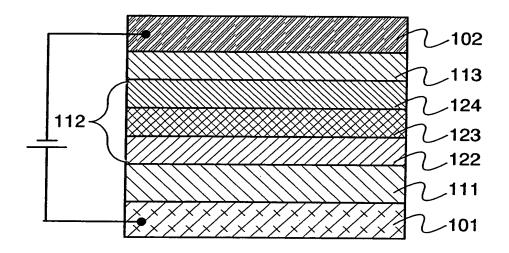


FIG. 2

**(** )

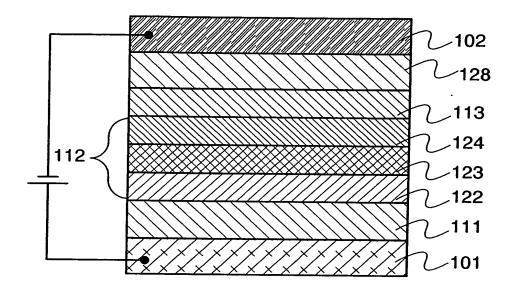


FIG. 3

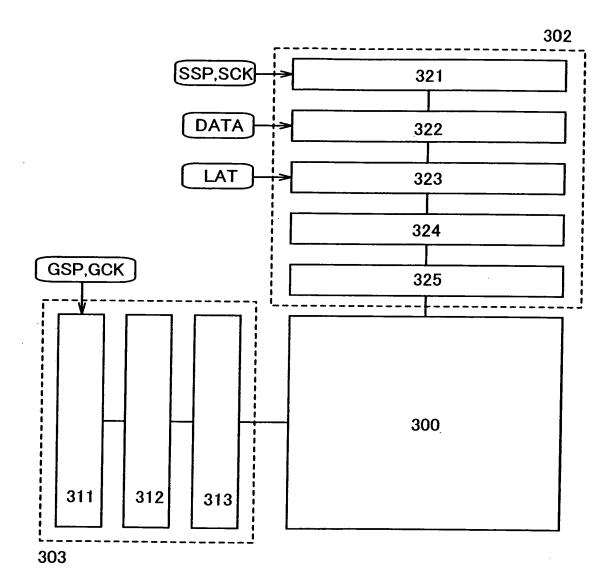


FIG. 4A

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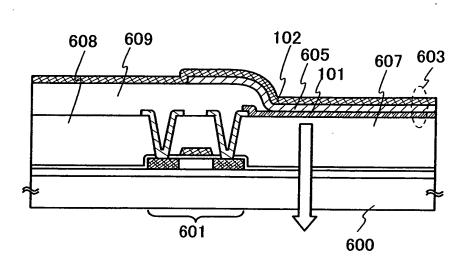


FIG. 4B

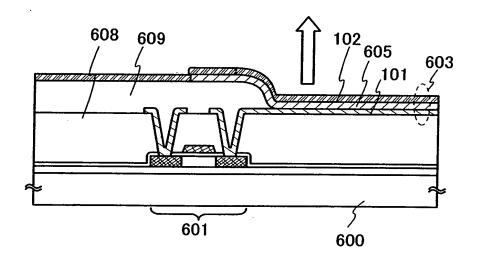
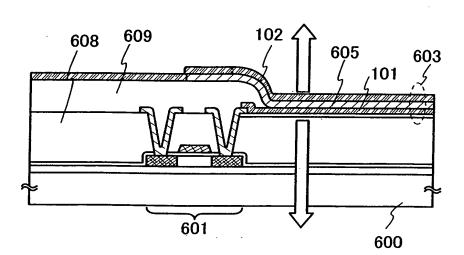


FIG. 4C



 $\widehat{(\ )}$ 

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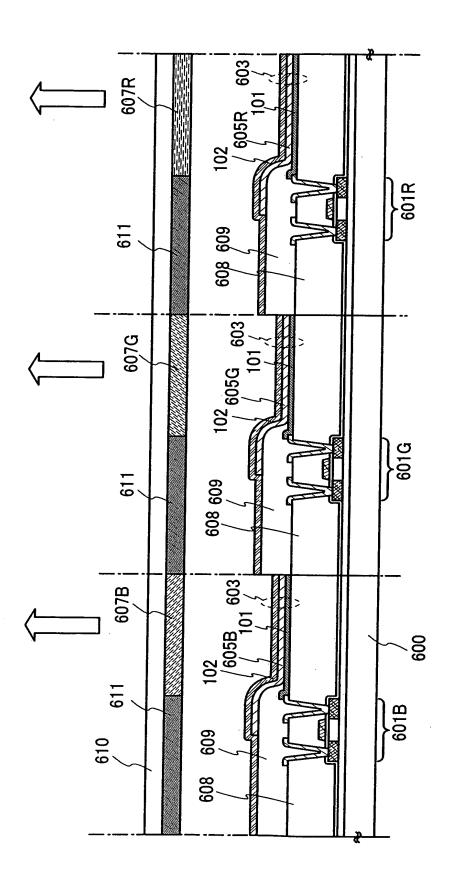


FIG. 5

 $(\hat{\phantom{a}})$ 

( )

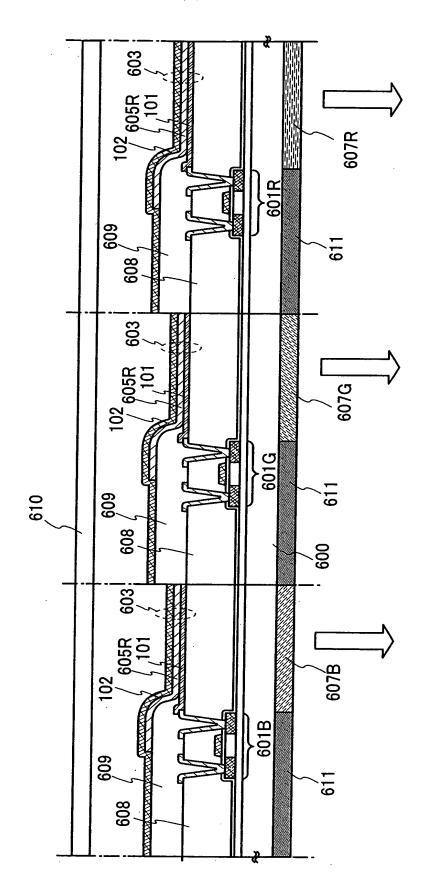


FIG. 6

FIG. 7A

FIG. 7B

( )

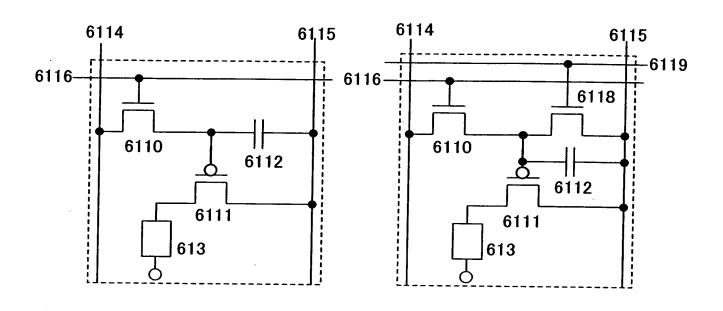
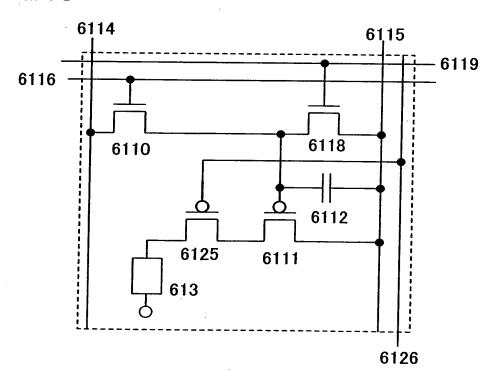
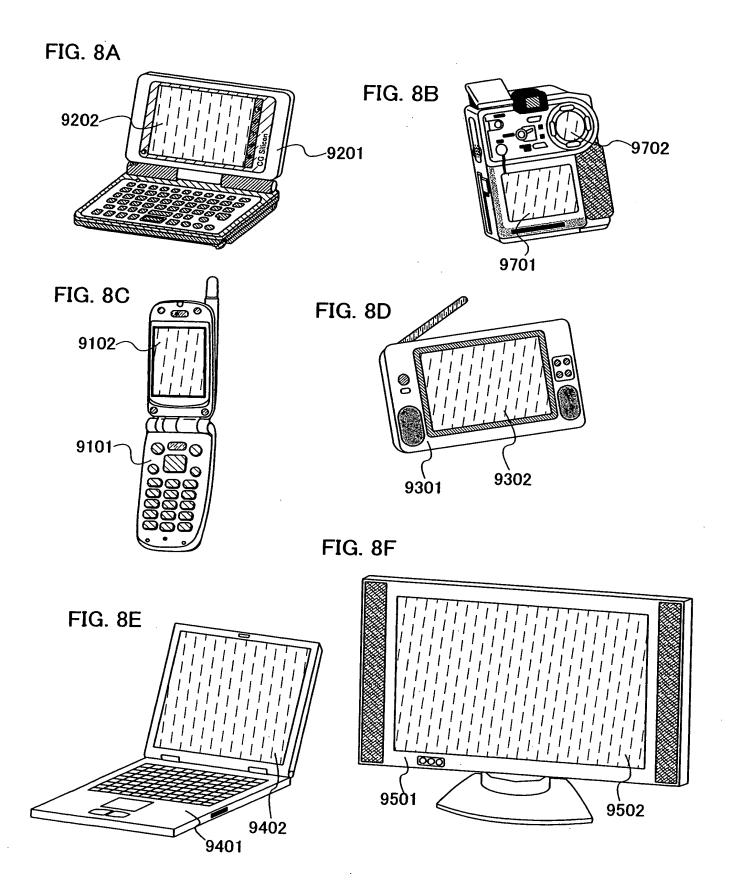


FIG. 7C



( )



## **EXPLANATION OF REFERENCE**

101: first electrode; 102: second electrode; 111: first layer; 112: second layer; 113: third layer; 122: hole transporting layer; 123: light emitting layer; 124: electron transporting layer; 128: fourth layer; 300: pixel portion; 302: signal line driver circuit; 303: scanning line driver circuit; 311: shift register; 312: level shifter; 313: buffer; 321: shift register; 322: first latch; 323: second latch; 324: level shifter; 325: buffer; 600: substrate; 601: TFT; 603: light emitting element; 605: electroluminescent layer; 608: interlayer insulating film; 609: partition; 610: opposed substrate; 611: resin; 613: light emitting element; 6110: transistor; 6111: transistor; 6112: capacitor; 6114: signal line; 6115: power supply line; 6116: scanning line; 6118: transistor; 6119: scanning line 6125: transistor; 9201: main body; 9202: display portion; 9701: display portion; 9702: display portion; 9101: main body; 9102: display portion; 9301: main body; 9302: display portion; 9401: main body; 9402: display portion; 9501: main body; 9502: display portion.